

# FAST Gradient Based Yield Optimization of Nonlinear Circuits

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**Abstract**—This paper meets the challenge of yield optimization of nonlinear microwave circuits operating in the steady state under large-signal periodic excitations. Yield-driven design is formulated as a one-sided  $\ell_1$  optimization problem. We introduce two novel, high-speed methods of gradient calculation, the integrated gradient approximation technique (IGAT) and the feasible adjoint sensitivity technique (FAST). IGAT utilizes the Broyden formula with special iterations of Powell to update the approximate gradients. FAST combines the efficiency and accuracy of the adjoint sensitivity technique with the simplicity of the perturbation technique. IGAT and FAST are compared with the simple perturbation approximate sensitivity technique (PAST) on the one extreme and the theoretical exact adjoint sensitivity technique (EAST) on the other. FAST, linking state-of-the-art optimization and efficient harmonic balance simulation, is the key to making our approach to nonlinear microwave circuit design the most powerful available. A FET frequency doubler example treats statistics of both linear elements and nonlinear device parameters. This design has six optimizable variables including input power and bias conditions, and 34 statistical parameters. Using either IGAT or FAST, yield is driven from 40% to 70%. FAST exhibits superior efficiency.

## I. INTRODUCTION

STATISTICAL circuit design has been recognized as an indispensable tool for modern CAD of integrated circuits [1]–[3]. A number of algorithms for yield optimization have been developed within the past 15 years, e.g., Director and Hachtel [4] (simplicial approximation), Soin and Spence [5] (the center of gravity method), Bandler and Abdel-Malek [6], [7] (updated approximations and cuts), Styblinski and Ruszczynski [8] (stochastic approximation), Polak and Sangiovanni-Vincentelli [9] (outer approximation), Singhal and Pinel [10] (parametric sampling), Bandler and Chen [3] (generalized  $\ell_p$  centering), and Biernacki *et al.* [11] (efficient quadratic approximation). This paper deals with yield optimization of nonlinear microwave circuits within the harmonic balance (HB) simulation environment.

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Statistical design of practical nonlinear microwave circuits is a challenge. One serious inherent difficulty is the potentially prohibitively high computational cost: many circuits have to be simulated repeatedly and each circuit simulation involves CPU-intensive iterations to solve the HB equations. Furthermore, gradient-based optimization requires effort to estimate the gradients of the error functions. Therefore, an effective and efficient approach to gradient calculation is of utmost importance.

The conventional perturbation approximate sensitivity technique (PAST) is conceptually simple. Since PAST needs to perturb all variables one at a time, the computational effort involved grows in proportion to the number of variables. Rizzoli *et al.* [12] used this method in their single-loop approach for nominal circuit design. In yield optimization, however, PAST becomes extremely inefficient because of the large number of circuit outcomes to be dealt with.

The exact adjoint sensitivity technique (EAST) has been recently developed by Bandler, Zhang, and Biernacki [13], [14] for the HB technique. In contrast to PAST, EAST involves solving a set of linear equations whose coefficient matrix is available after circuit simulation. The solution of a single adjoint system is sufficient for the calculation of sensitivities with respect to all variables. No perturbation or iterative simulations are required. EAST enjoys high computational efficiency, but is very difficult to implement.

In this paper, we formulate the yield-driven design of nonlinear circuits as a one-sided  $\ell_1$  optimization problem [15] allowing the powerful, robust one-sided  $\ell_1$  algorithm proposed by Bandler *et al.* [16] to be employed. We introduce two powerful approaches to gradient calculation. One is the integrated gradient approximation technique (IGAT), presented by Bandler *et al.* [17], which is adapted here to the needs of yield optimization. The other is the feasible adjoint sensitivity technique (FAST), first reported by Bandler *et al.* [18]. Motivated by the potential impact of the adjoint sensitivity approach on general-purpose CAD programs, we have studied its implementational aspects. FAST is demonstrated to be an implementable, high-speed gradient calculation technique. FAST retains most of the efficiency and accuracy of EAST while accommodating the simplicity of PAST.

IGAT and FAST are applied to yield optimization of a microwave frequency doubler. In this example, normal and uniform distributions describing large-signal FET model parameters and passive elements are fully accommodated. The performance yield is increased from 40% to 70%.

In Section II we formulate the yield optimization problem for nonlinear circuits as a one-sided  $\mathcal{L}_1$  optimization problem. Sections III and IV are devoted to IGAT and FAST, respectively. Comparisons between the various approaches are made in Section V. Section VI presents the details of the FET frequency doubler example.

## II. THE YIELD PROBLEM FOR NONLINEAR CIRCUITS

### A. Specifications and Errors for Nonlinear Circuit Yield Optimization

Consider a nonlinear microwave circuit operating under large-signal, steady-state periodic conditions. Response functions for such a circuit may involve dc and harmonic components of the output signal. Therefore, design specifications can be imposed at dc and several harmonics. The  $j$ th specification can be denoted by

$$S_{uj}(\mathbf{h}) \quad (1a)$$

if it is an upper specification, or

$$S_{lj}(\mathbf{h}) \quad (1b)$$

in the case of lower specifications, where

$$\mathbf{h} = [0 \quad 1 \quad 2 \quad \cdots \quad H]^T \quad (2)$$

is the harmonic index vector, and 0 and  $H$  represent dc and the highest harmonic, respectively. A specific circuit response may involve all or some of the  $(H+1)$  spectral components.

Manufactured outcomes are spread over a region which can be described by the nominal design,  $\phi^0$ , along with a statistical distribution of parameters. Parameters in  $\phi^0$  can be lumped element values, device model parameters, dimensions of a physical realization, etc. For statistical design, many circuits are needed to represent the distribution of manufactured outcomes. Such circuit outcomes, denoted by  $\phi^i$ , can be written as

$$\phi^i = \phi^0 + \Delta\phi^i, \quad i = 1, 2, \dots, N \quad (3)$$

where  $\Delta\phi^i$  is the deviation of the  $i$ th outcome from the nominal circuit and  $N$  is the number of outcomes considered. In yield optimization, each  $\phi^i$  is determined by statistically perturbing  $\phi^0$  according to a known or assumed statistical distribution of the manufactured outcomes.

The response of each outcome, denoted by

$$R_j(\phi^i, \mathbf{h}) \quad (4)$$

is calculated after solving the HB equations [19]

$$\bar{F}(\phi^i, \bar{V}) = \mathbf{0} \quad (5)$$

where  $\bar{V}$  comprises the split real and imaginary parts of

the state variables in the HB equation. The corresponding error function is defined as

$$R_j(\phi^i, \mathbf{h}) - S_{uj}(\mathbf{h}) \quad (6a)$$

or as

$$S_{lj}(\mathbf{h}) - R_j(\phi^i, \mathbf{h}). \quad (6b)$$

We assemble all errors for the outcome  $\phi^i$  into one vector  $e^i$ . If all entries of this vector are nonpositive, the outcome  $\phi^i$  represents an acceptable circuit.

For  $N$  statistical outcomes generated, the production yield can be estimated by

$$Y \approx \frac{\text{number of acceptable circuits}}{N}. \quad (7)$$

### B. Formulation of One-Sided $\mathcal{L}_1$ Objective Function

The problem of yield optimization can be properly converted to a mathematical programming problem so that modern mathematical optimization techniques can be applied. In the following the design variables are nominal values  $\phi^0$ . Although only the outcomes  $\phi^i$  appear in the error functions, they depend on  $\phi^0$  because the  $\phi^i$  are related to  $\phi^0$ .

After the error vector  $e^i$  for the outcome  $\phi^i$  has been assembled as

$$e^i = [e_1(\phi^i) \quad e_2(\phi^i) \cdots e_M(\phi^i)]^T \quad (8)$$

where  $M$  is the total number of errors considered, the formulation of the objective function for optimization can follow the procedure described in [3]. First, we create the generalized  $\mathcal{L}_p$  function  $v^i$  from  $e^i$  [3]:

$$v^i = \begin{cases} \left[ \sum_{j \in J(\phi^i)} (e_j(\phi^i))^p \right]^{1/p} & \text{if } J(\phi^i) \neq \emptyset \quad (9a) \\ - \left[ \sum_{j=1}^M (-e_j(\phi^i))^{-p} \right]^{-1/p} & \text{if } J(\phi^i) = \emptyset \quad (9b) \end{cases}$$

where

$$J(\phi^i) = \{j | e_j(\phi^i) \geq 0\}. \quad (10)$$

Then we define the one-sided  $\mathcal{L}_1$  objective function for yield optimization [3] as

$$U(\phi^0) = \sum_{i \in I} \alpha_i v^i \quad (11)$$

where

$$I = \{i | v^i > 0\} \quad (12)$$

and  $\alpha_i$  are positive multipliers. If the  $\alpha_i$  were chosen as [3]

$$\alpha_i = \frac{1}{|v^i|} \quad (13)$$

then the function  $U(\phi^0)$  would become the exact number

of unacceptable circuits and the yield would be

$$Y(\phi^0) = 1 - \frac{U(\phi^0)}{N}. \quad (14)$$

The mechanism of the one-sided  $\ell_1$  function naturally imitates the relation between the yield and unacceptable or acceptable outcomes. Now, the task of maximizing yield  $Y$  is converted to one of minimizing  $U(\phi^0)$ ; that is,

$$\text{minimize}_{\phi^0} U(\phi^0). \quad (15)$$

We use (13) to assign multipliers  $\alpha_i$  at the starting point and fix them during the optimization process. Then  $U(\phi^0)$  is no longer the count of unacceptable outcomes during optimization, but a continuous approximate function to it.

Suppose the value of  $p$  is chosen as 1. The objective function for the one-sided  $\ell_1$  optimization becomes

$$U(\phi^0) = \sum_{i \in I} \sum_{j \in J(\phi^i)} \alpha_i e_j(\phi^i) \quad (16)$$

where  $\alpha_i$ ,  $I$ , and  $J(\phi^i)$  are defined as before. In (16), error functions for optimization are  $e_j(\phi^i)$ . In (11), error functions for optimization are  $v^i$ . The functions  $e_j(\phi^i)$  are differentiable, but the functions  $v^i$  of (9) may not be. Therefore, we use (16) in our yield optimization.

Several reoptimizations with updated  $\alpha_i$  may be applied to further increase yield. Each can use a different number of statistical outcomes or a different set of outcomes.

### C. The One-Sided $\ell_1$ Optimization Algorithm

We use a highly efficient one-sided  $\ell_1$  optimization algorithm [16] to solve (15). The algorithm is based on a two-stage method combining a first-order method, the trust region Gauss–Newton method, with a second-order method, the quasi-Newton method. Switching between the two methods is automatically done to ensure global convergence of the combined algorithm.

To summarize the discussion in this section, all steps involved in our yield optimization are shown in Fig. 1.

## III. INTEGRATED GRADIENT APPROXIMATION TECHNIQUE

After a review of PAST, IGAT is discussed for the case of a single function. The application of IGAT in yield optimization follows.

Because the application of IGAT is not restricted to circuit response functions, let us use  $f(\phi)$  to denote a generic function.

### A. Approximating Derivatives by PAST

The first-order derivative of  $f(\phi)$  with respect to the  $k$ th variable can be estimated by

$$\frac{\partial f(\phi)}{\partial \phi_k} \approx \frac{f(\phi + \Delta\phi_k \mathbf{u}_k) - f(\phi)}{\Delta\phi_k} \quad (17)$$

where  $\phi + \Delta\phi_k \mathbf{u}_k$  denotes the perturbation of the  $k$ th

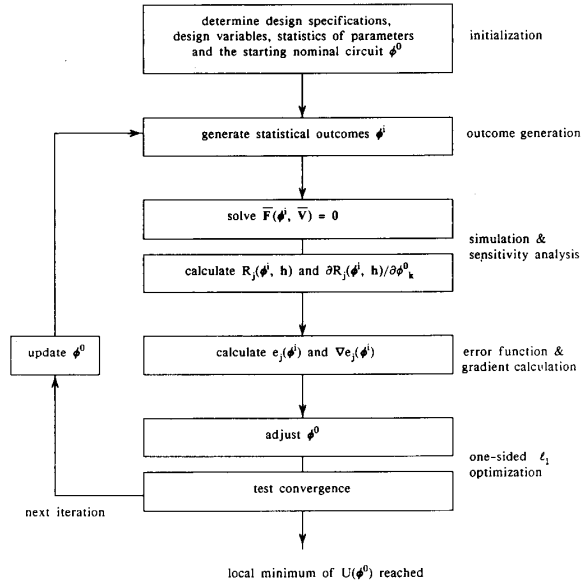


Fig. 1. Flowchart for yield optimization.

variable,  $\Delta\phi_k$  is the perturbation length and  $\mathbf{u}_k$  is a column vector which has 1 in the  $k$ th position and zeros elsewhere. An approximation to the gradient,  $\nabla f(\phi)$ , can be obtained by perturbing all variables one at a time.

### B. IGAT for General Functions [17]

To start the process, PAST is used as in (17) to calculate the approximate gradient.

The Broyden update generates the new approximate gradient from the previous gradient,

$$\begin{aligned} \nabla f(\phi_{\text{new}}) &= \nabla f(\phi_{\text{old}}) \\ &+ \frac{f(\phi_{\text{new}}) - f(\phi_{\text{old}}) - (\nabla f(\phi_{\text{old}}))^T \Delta\phi}{\Delta\phi^T \Delta\phi} \Delta\phi \end{aligned} \quad (18)$$

where  $\phi_{\text{old}}$  and  $\phi_{\text{new}}$  are two different points and  $\Delta\phi = \phi_{\text{new}} - \phi_{\text{old}}$ . If  $\phi_{\text{old}}$  and  $\phi_{\text{new}}$  are iterates of optimization,  $f(\phi_{\text{old}})$  and  $f(\phi_{\text{new}})$  need to be evaluated anyway. Thus the updated gradient can be obtained without additional function evaluations (circuit simulations).

To overcome a particular deficiency of the Broyden update, after a few updates, a special iteration of Powell generates a special step  $\Delta\phi$  to guarantee strictly linearly independent directions. After a number of optimization iterations, we may also apply PAST (17) to maintain the accuracy of the approximate gradients at a desirable level.

### C. Application of IGAT in Yield Optimization

In circuit simulation, there are usually several response levels involved. Suppose the response of interest, on which the design specification is imposed, is the power gain. In the circuit simulation, the power gain is calculated from the output power, which, in turn, is calculated from the

output voltage. This implies three different response levels. IGAT can be applied at any response level. We still use  $f$  to denote a particular response function whose gradient is to be approximated.

Because the nominal values,  $\phi^0$ , are design variables, all perturbations are made to  $\phi^0$  in the initialization and reinitialization steps using PAST (17). When  $\phi^0$  is perturbed to  $\phi^0 + \Delta\phi_k^0 \mathbf{u}_k$ , denoted by  $\phi_{k,\text{pert}}^0$  for short, outcomes should be regenerated from  $\phi_{k,\text{pert}}^0$  in order to get perturbed circuit responses. These outcomes are denoted by  $\phi_{k,\text{pert}}^i$ . Then from (17), the approximate derivative of the response  $f(\phi^i)$  is defined as

$$\frac{\partial f(\phi^i)}{\partial \phi_k^0} \approx \frac{f(\phi_{k,\text{pert}}^i) - f(\phi^i)}{\Delta\phi_k^0}. \quad (19)$$

When the Broyden update or the special iteration of Powell is used,  $\Delta\phi^0$  is computed from  $\phi_{\text{old}}^0$  and  $\phi_{\text{new}}^0$  as generated either by the optimizer or by the special iteration. Outcomes  $\phi_{\text{old}}^i$  and  $\phi_{\text{new}}^i$  are outcomes generated from  $\phi_{\text{old}}^0$  and  $\phi_{\text{new}}^0$ , respectively. The gradient of the response  $f(\phi^i)$  w.r.t.  $\phi^0$  can be updated as

$$\begin{aligned} \nabla f(\phi_{\text{new}}^i) &= \nabla f(\phi_{\text{old}}^i) \\ &+ \frac{f(\phi_{\text{new}}^i) - f(\phi_{\text{old}}^i) - (\nabla f(\phi_{\text{old}}^i))^T \Delta\phi^0}{(\Delta\phi^0)^T \Delta\phi^0} \Delta\phi^0. \end{aligned} \quad (20)$$

#### IV. FEASIBLE ADJOINT SENSITIVITY TECHNIQUE

In the HB simulation environment, the sensitivity of a response with respect to one variable,  $\phi_k$ ,

$$\frac{\partial R_j(\phi, \mathbf{h})}{\partial \phi_k} \quad (21)$$

should be computed subject to the constraints of the HB equation, e.g., [19]

$$\bar{\mathbf{F}}(\phi, \bar{\mathbf{V}}) = \mathbf{0}. \quad (22)$$

Bandler *et al.* [13], [14] proposed EAST, and the sensitivity expressions for various elements were derived and listed [14].

Here, we propose FAST, which is also based on adjoint sensitivity principles. Suppose that the circuit is divided

into linear and nonlinear subnetworks and that the response of interest is the voltage at the output port. Normally, the response is taken from the linear subnetwork.

The response can be calculated by

$$\bar{\mathbf{V}}_{\text{out}} = [\mathbf{a}^T \quad \mathbf{b}^T] \begin{bmatrix} \bar{\mathbf{V}} \\ \bar{\mathbf{V}}_s \end{bmatrix} = \mathbf{c}^T \begin{bmatrix} \bar{\mathbf{V}} \\ \bar{\mathbf{V}}_s \end{bmatrix} \quad (23)$$

where  $\bar{\mathbf{V}}_s$  denotes the split real and imaginary parts in the spectra of excitation voltages,  $\bar{\mathbf{V}}$  denotes the solution to the HB equations (22), and

$$\mathbf{c} = [\mathbf{a}^T \quad \mathbf{b}^T]^T \quad (24)$$

is a linear transfer vector linking the output voltage with  $\bar{\mathbf{V}}_s$  and  $\bar{\mathbf{V}}$ .  $\bar{\mathbf{V}}$  and  $\mathbf{c}$  are functions of  $\phi$ .  $\bar{\mathbf{V}}_s$  can also be a function of  $\phi$  if we want to change  $\bar{\mathbf{V}}_s$  to improve the circuit performance.

#### A. FAST for the Nominal Circuit Case

To make the derivation procedure concise, we concentrate on a single circuit design with variables  $\phi$ . From (23) the approximate derivative of  $\bar{\mathbf{V}}_{\text{out}}$  w.r.t.  $\phi_k$  can be calculated as

$$\frac{\Delta \bar{\mathbf{V}}_{\text{out}}}{\Delta \phi_k} \approx \frac{\Delta \mathbf{c}^T}{\Delta \phi_k} \begin{bmatrix} \bar{\mathbf{V}} \\ \bar{\mathbf{V}}_s \end{bmatrix} + \mathbf{a}^T \frac{\Delta \bar{\mathbf{V}}}{\Delta \phi_k} + \mathbf{b}^T \frac{\Delta \bar{\mathbf{V}}_s}{\Delta \phi_k} \quad (25)$$

by perturbing  $\phi$  to  $\phi + \Delta\phi_k \mathbf{u}_k$ . Let  $\hat{\bar{\mathbf{V}}}$  be the adjoint voltages obtained by solving the set of linear equations

$$\mathbf{J}^T \hat{\bar{\mathbf{V}}} = \mathbf{a} \quad (26)$$

where  $\mathbf{J}$  is the Jacobian of  $\bar{\mathbf{F}}$  w.r.t.  $\bar{\mathbf{V}}$  at the HB solution. We can express

$$\begin{aligned} \Delta \bar{\mathbf{V}}_{\text{out}} &\approx [\mathbf{c}^T(\phi + \Delta\phi_k \mathbf{u}_k) - \mathbf{c}^T] \begin{bmatrix} \bar{\mathbf{V}} \\ \bar{\mathbf{V}}_s \end{bmatrix} \\ &+ \mathbf{b}^T [\bar{\mathbf{V}}_s(\phi + \Delta\phi_k \mathbf{u}_k) - \bar{\mathbf{V}}_s] - \hat{\bar{\mathbf{V}}}^T \Delta \bar{\mathbf{F}}. \end{aligned} \quad (27)$$

The incremental term  $\Delta \bar{\mathbf{F}}$  can be approximated by

$$\Delta \bar{\mathbf{F}} \approx \bar{\mathbf{F}}(\phi + \Delta\phi_k \mathbf{u}_k, \bar{\mathbf{V}}) \quad (28)$$

for a small  $\Delta\phi_k$ .

Considering the different elements, (27) can be further expressed as

$$\Delta \bar{\mathbf{V}}_{\text{out}} \approx \begin{cases} [\mathbf{c}^T(\phi + \Delta\phi_k \mathbf{u}_k) - \mathbf{c}^T] \begin{bmatrix} \bar{\mathbf{V}} \\ \bar{\mathbf{V}}_s \end{bmatrix} - \hat{\bar{\mathbf{V}}}^T \bar{\mathbf{F}}(\phi + \Delta\phi_k \mathbf{u}_k, \bar{\mathbf{V}}), & \phi_k \in \text{linear subnetwork} \\ \mathbf{b}^T [\bar{\mathbf{V}}_s(\phi + \Delta\phi_k \mathbf{u}_k) - \bar{\mathbf{V}}_s] - \hat{\bar{\mathbf{V}}}^T \bar{\mathbf{F}}(\phi + \Delta\phi_k \mathbf{u}_k, \bar{\mathbf{V}}), & \phi_k \in \text{sources} \\ -\hat{\bar{\mathbf{V}}}^T \bar{\mathbf{F}}(\phi + \Delta\phi_k \mathbf{u}_k, \bar{\mathbf{V}}), & \phi_k \in \text{nonlinear subnetwork.} \end{cases} \quad (29)$$

This formula is much easier to implement than the corresponding formula for EAST [13], [14]. The function  $\bar{F}(\phi + \Delta\phi_k \mathbf{u}_k, \bar{V})$  is evaluated by perturbation. The effort for solving the linear equations (26) is small since the LU factors of the Jacobian matrix are already available from the final HB iteration. The terms  $\bar{V}$  and  $\bar{V}_s$  are also available from the HB simulation. The perturbed vectors  $\mathbf{a}(\phi + \Delta\phi_k \mathbf{u}_k)$  and  $\mathbf{b}(\phi + \Delta\phi_k \mathbf{u}_k)$  can be easily calculated since they involve the linear subnetwork only. Finally, the perturbed excitations  $\bar{V}_s(\phi + \Delta\phi_k \mathbf{u}_k)$  can be effortlessly obtained. It is clear that the calculation of all the terms in (27) or (29) can be readily implemented.

Finally, the approximate sensitivity of output voltage  $\bar{V}_{\text{out}}$  w.r.t.  $\phi_k$  can be computed as

$$\frac{\partial \bar{V}_{\text{out}}}{\partial \phi_k} \approx \frac{\Delta \bar{V}_{\text{out}}}{\Delta \phi_k}. \quad (30)$$

### B. FAST for Yield Optimization

Similar to IGAT perturbations in yield optimization, perturbations used in FAST are also made to the nominal values  $\phi^0$ . The outcomes  $\phi^i$  and  $\phi_{k,\text{pert}}^i$  are generated from the unperturbed and perturbed nominal values  $\phi^0$  and  $\phi_{k,\text{pert}}^0$ , respectively. The increment of the output voltage of the  $i$ th outcome due to the perturbation is calculated by

$$\begin{aligned} \Delta \bar{V}_{\text{out}}(\phi^i) \approx & \left[ \mathbf{c}^T(\phi_{k,\text{pert}}^i) - \mathbf{c}^T(\phi^i) \right] \begin{bmatrix} \bar{V} \\ \bar{V}_s(\phi^i) \end{bmatrix} \\ & + \mathbf{b}^T(\phi^i) \left[ \bar{V}_s(\phi_{k,\text{pert}}^i) - \bar{V}_s(\phi^i) \right] \\ & - \hat{\bar{V}}^T \Delta \bar{F} \end{aligned} \quad (31)$$

where

$$\Delta \bar{F} \approx \bar{F}(\phi_{k,\text{pert}}^i, \bar{V}). \quad (32)$$

$\bar{V}$  is the solution to the HB equation (5), and  $\hat{\bar{V}}$  is the solution to (26).

## V. COMPARISONS OF VARIOUS APPROACHES

### A. Comparisons of PAST, IGAT, EAST, and FAST

PAST and IGAT do not need any modification of the circuit simulator.

PAST is a widely used approach, because it is very easy to implement. However, the cost may be prohibitive. Suppose there are ten design variables in the nonlinear circuit. Using PAST to calculate the gradient, one needs to perturb all design variables and to solve the entire nonlinear circuit for each perturbation, i.e., ten times. The best possible situation for this approach is that all ten simulations use the same Jacobian and all converge in one iteration. This applies to nominal circuit design. For yield optimization, a large number of statistically generated circuit outcomes may make PAST prohibitive.

The distinct advantage of IGAT over PAST is that IGAT only requires the circuit response function once to update the previously calculated gradient for most optimization iterations. IGAT enjoys the simplicity of the perturbation method so that yield optimization can be

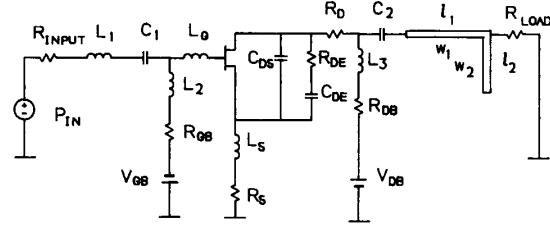


Fig. 2. Circuit diagram of the FET microwave frequency doubler. The nominal values for nonoptimizable variables are:  $L_2 = 15$  nH,  $L_3 = 15$  nH,  $C_1 = 20$  pF,  $C_2 = 20$  pF,  $w_1 = 0.1 \times 10^{-3}$  m,  $w_2 = 0.635 \times 10^{-3}$  m,  $R_{\text{LOAD}} = R_{\text{INPUT}} = 50 \Omega$ , and  $R_{GB} = R_{DB} = 10 \Omega$ .

carried out without modifying the circuit simulator to calculate exact derivatives. IGAT is very desirable when the circuit simulator cannot be modified.

Both EAST and FAST require modification to the circuit simulator.

The generic exact adjoint sensitivity technique [13], [14] is accepted by all circuit theoreticians as the most powerful tool. However, to implement it, we have to keep track of all arbitrary locations of variables and to compute branch voltages at all these locations. Microwave software engineers have, to date, found these obstacles insurmountable.

Using FAST, we also need to perturb all variables. For a circuit with ten design variables, instead of completely solving ten nonlinear circuits, we only evaluate ten residuals in the form of (28) and calculate the perturbed linear subnetwork. The solution of adjoint equation (26) can be accomplished by using forward and backward substitutions. In FAST, we completely eliminate the need to track variable locations. We only need to identify the output port, which is the simplest step in adjoint sensitivity theory.

### B. Numerical Comparison of FAST, EAST, and PAST

We use a MESFET mixer [13], [14] to investigate the accuracy and actual time efficiency of FAST [18]. Sensitivities of the mixer conversion gain w.r.t. 26 variables were calculated by the FAST, EAST, and PAST approaches, respectively. The variables included all parameters in the linear as well as the nonlinear part, dc bias, LO power, and IF, LO, and RF terminations. The results show that the FAST sensitivities are almost identical to the exact sensitivities, whereas the sensitivities computed by PAST are typically 1 to 2% different from their exact values. This fact reveals that FAST promises to be much more reliable than PAST. The CPU time comparison shows that FAST is three times slower than EAST but 23 times faster than PAST for one complete sensitivity analysis of the mixer circuit.

## VI. YIELD OPTIMIZATION OF A FREQUENCY DOUBLER

The FET frequency doubler shown in Fig. 2 is considered. It consists of a common-source FET with a lumped input matching network and a microstrip output matching

TABLE I  
ASSUMED STATISTICAL DISTRIBUTIONS FOR THE FET PARAMETERS

FET Parameter	Nominal Value	Standard Deviation (%)	FET Parameter	Nominal Value	Standard Deviation (%)
$L_G$ (nH)	0.16	5	$S_I$	$0.676 \times 10^{-1}$	0.65
$R_D$ ( $\Omega$ )	2.153	3	$K_G$	1.1	0.65
$L_S$ (nH)	0.07	5	$\tau$ (pS)	7.0	6
$R_S$ ( $\Omega$ )	1.144	5	$S_S$	$1.666 \times 10^{-3}$	0.65
$R_{DE}$ ( $\Omega$ )	440	14	$I_{G0}$ (A)	$0.713 \times 10^{-5}$	3
$C_{DE}$ (pF)	1.15	3	$\alpha_G$	38.46	3
$C_{DS}$ (pF)	0.12	4.5	$I_{B0}$ (A)	$-0.713 \times 10^{-5}$	3
$I_{DSS}$ (A)	$6.0 \times 10^{-2}$	5	$\alpha_B$	-38.46	3
$V_{P0}$ (V)	-1.906	0.65	$R_{10}$ ( $\Omega$ )	3.5	8
$\gamma$	$-15 \times 10^{-2}$	0.65	$C_{10}$ (pF)	0.42	4.16
$E$	1.8	0.65	$C_{F0}$ (pF)	0.02	6.64

The following parameters are considered as deterministic:  
 $K_E = 0.0$ ,  $K_R = 1.111$ ,  $K_I = 1.282$ ,  $C_{1S} = 0.0$ , and  $K_F = 1.282$ .  
For definitions of the FET parameters, see [20].

and filter section. The optimization variables include the input inductance  $L_1$  and the microstrip lengths  $l_1$  and  $l_2$ . Two bias voltages  $V_{GB}$  and  $V_{DB}$  and the driving power level  $P_{IN}$  are also considered as optimization variables. The fundamental frequency is 5 GHz. Responses of interest are the conversion gain and spectral purity, which are defined by

$$\text{conversion gain} = 10 \log \frac{\text{power of the second harmonic at the output port}}{\text{power of the fundamental frequency at the input port}}$$

and

$$\text{spectral purity} = 10 \log \frac{\text{power of the second harmonic at the output port}}{\text{total power of all other harmonics at the output port}}$$

respectively. The specifications for the conversion gain and spectral purity are 2.5 dB and 20 dB, respectively. They are both lower specifications.

Our large-signal FET statistical model includes an intrinsic large-signal FET model modified from the Materka and Kacprzak model [21], statistical distributions, and correlations of parameters. The multidimensional normal distribution is assumed for all FET intrinsic and extrinsic parameters. The means and standard deviations are listed in Table I. The correlations between parameters are assumed according to the results published by Purviance *et al.* [22]. Certain modifications have been made to make the correlations for the large-signal FET model consistent with those for the small-signal FET model dealt with in [22]. The correlation coefficients are given in Table II. Uniform distributions with fixed tolerances of 3% are assumed for  $P_{IN}$ ,  $V_{GB}$ ,  $V_{DB}$ ,  $L_1$ ,  $l_1$ , and  $l_2$ . Finally, uniform distributions with fixed tolerances of 5% are assumed for  $L_2$ ,  $L_3$ ,  $C_1$ ,  $C_2$ ,  $w_1$  and  $w_2$ . The random number generator used is capable of generating outcomes from the independent and multidimensional correlated normal distributions and from uniform distributions.

In our program, the formulation (16) is used. In more detail, the error functions resulting from the simulated conversion gain and spectral purity are calculated, and

TABLE II  
FET MODEL PARAMETER CORRELATIONS [22]

	$L_G$	$R_S$	$L_S$	$R_{DE}$	$C_{DS}$	$\beta_m$	$\tau$	$R_{IN}$	$C_{GS}$	$C_{GD}$
$L_G$	1.00	-0.16	0.11	-0.22	-0.20	0.15	0.06	0.15	0.25	0.04
$R_S$	-0.16	1.00	-0.28	0.02	0.06	-0.09	-0.16	0.12	-0.24	0.26
$L_S$	0.11	-0.28	1.00	0.11	-0.26	0.53	0.41	-0.52	0.78	-0.12
$R_{DE}$	-0.22	0.02	0.11	1.00	-0.44	0.03	0.04	-0.54	0.02	-0.14
$C_{DS}$	-0.20	0.06	-0.26	-0.44	1.00	-0.13	-0.14	0.23	-0.24	-0.04
$\beta_m$	0.15	-0.09	0.53	0.03	-0.13	1.00	-0.08	-0.26	0.78	0.38
$\tau$	0.06	-0.16	0.41	0.04	-0.14	-0.08	1.00	-0.19	0.27	-0.46
$R_{IN}$	0.15	0.12	-0.52	-0.54	0.23	-0.26	-0.19	1.00	-0.35	0.05
$C_{GS}$	0.25	-0.24	0.78	0.02	-0.24	0.78	0.27	-0.35	1.00	0.15
$C_{GD}$	0.04	0.26	-0.12	-0.14	-0.04	0.38	-0.46	0.05	0.15	1.00

Certain modifications have been made to adjust these small-signal parameter correlations to be consistent with the large-signal FET model.

then these error functions with their multipliers defined in (13) are fed into the one-side  $\ell_1$  optimizer. IGAT and FAST are implemented to provide gradients. IGAT calculates approximate sensitivities of the conversion gain and spectral purity. The computer used is a Multiflow Trace 14/300.<sup>1</sup>

The starting point for yield optimization is the solution of the minimax nominal design w.r.t. the same specifica-

tions, using the same six design variables. At this point, the estimated yield based on 500 outcomes is 39.6%.

We conduct two designs using IGAT and FAST gradient calculation in the same environment. Computational details are given in Tables III and IV. Each design has two consecutive phases; that is, the starting point for the second phase is the solution of the first phase. The second phase is to reoptimize the first solution with updated  $\alpha_i$ .

Using IGAT, the first phase reaches 71% yield, and the second phase confirms that the solution of the first phase has been optimized in terms of the estimated yield. The two phases use 61 optimization iterations and 184 function evaluations. For FAST, the first phase uses 19 function evaluations and gradient calculations to give 70.6% yield. The second phase slightly increases the estimated yield to 71%, verifying the solution of the first phase. The efficiency of FAST is well demonstrated. To reach the same yield level, the CPU time used by the FAST approach is much less than that used by the IGAT ap-

<sup>1</sup>A test at McMaster University using LINPACK to solve 100 linear equations indicated a performance for this machine of 10 MFLOPS. A VAX 11/780 exhibited 0.14 MFLOPS in a similar test.

TABLE III  
YIELD OPTIMIZATION OF THE FREQUENCY DOUBLER USING IGAT

Variable	Starting Point	Nominal Design	Solution I	Solution II
$P_{IN}(W)$	$2.0 \times 10^{-3}$	$2.49048 \times 10^{-3}$	$1.98488 \times 10^{-3}$	$1.92366 \times 10^{-3}$
$V_{GB}(V)$	-1.9	-1.70329	-1.93468	-1.92542
$V_{DB}(V)$	5.0	6.50000	6.50000	6.50000
$L_1(nH)$	5.0	5.29066	5.68905	5.63822
$l_1(m)$	$1.0 \times 10^{-3}$	$1.77190 \times 10^{-3}$	$1.73378 \times 10^{-3}$	$1.73740 \times 10^{-3}$
$l_2(m)$	$5.0 \times 10^{-3}$	$5.73087 \times 10^{-3}$	$5.75011 \times 10^{-3}$	$5.74907 \times 10^{-3}$
Yield		39.6%	71.0%	71.0%
No. of Optimization Iterations			23	38
No. of Function Evaluations			89	95
CPU Time (Multiflow Trace 14/300)			18.6min	19.1min

The yield is estimated from 500 outcomes.

TABLE IV  
YIELD OPTIMIZATION OF THE FREQUENCY DOUBLER USING FAST

Variable	Starting Point	Nominal Design	Solution I	Solution II
$P_{IN}(W)$	$2.0 \times 10^{-3}$	$2.49048 \times 10^{-3}$	$2.02313 \times 10^{-3}$	$1.94444 \times 10^{-3}$
$V_{GB}(V)$	-1.9	-1.70329	-1.93930	-1.92927
$V_{DB}(V)$	5.0	6.50000	6.50000	6.50000
$L_1(nH)$	5.0	5.29066	5.71547	5.63312
$l_1(m)$	$1.0 \times 10^{-3}$	$1.77190 \times 10^{-3}$	$1.73531 \times 10^{-3}$	$1.74046 \times 10^{-3}$
$l_2(m)$	$5.0 \times 10^{-3}$	$5.73087 \times 10^{-3}$	$5.74965 \times 10^{-3}$	$5.74956 \times 10^{-3}$
Yield		39.6%	70.6%	71.0%
No. of Optimization Iterations			19	29
No. of Function Evaluations and Sensitivity Analyses			19	29
CPU Time (Multiflow Trace 14/300)			7.9min	12.1min

The yield is estimated from 500 outcomes.

proach. Although IGAT is slower than FAST, it is very robust in terms of the final yield reached.

Figs. 3 and 4 show histograms of the conversion gain and the spectral purity, respectively. Five hundred outcomes are used to calculate both distributions. Fig. 3(a) is the conversion gain distribution before yield optimization. The histograms in parts (b) and (c) of Fig. 3 are based on the solutions using IGAT and FAST, respectively. Fig. 4(a) is the spectral purity distribution before yield optimization. The histograms in parts (b) and (c) of Fig. 4 are based on solutions using IGAT and FAST, respectively. The improvement in spectral purity is very clearly illustrated by the histograms in Fig. 4. Before yield optimization, the center of the distribution is close to the design specification of 20 dB, indicating that many outcomes are unacceptable. After yield optimization, the center of the distribution is shifted to the right-hand side of the specification. Most outcomes then satisfy the specification.

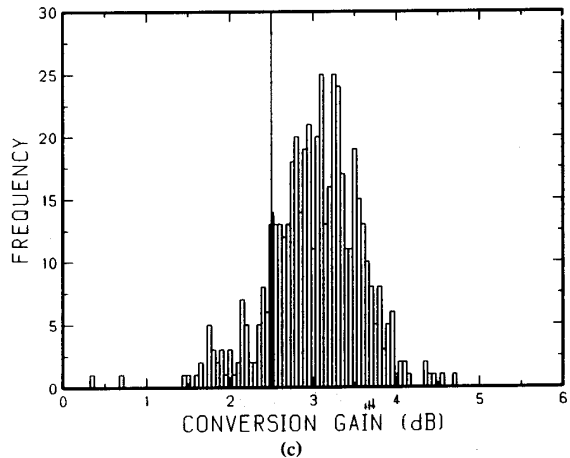
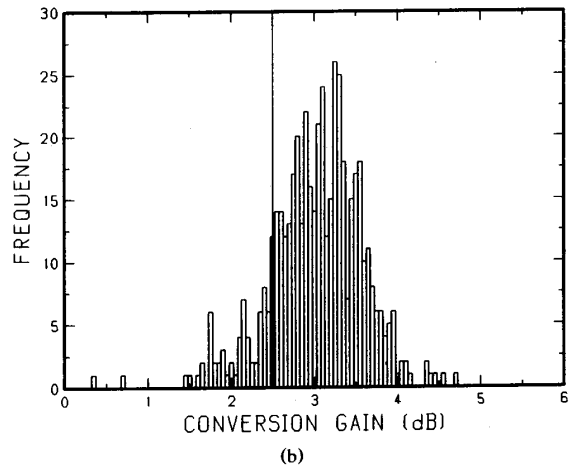
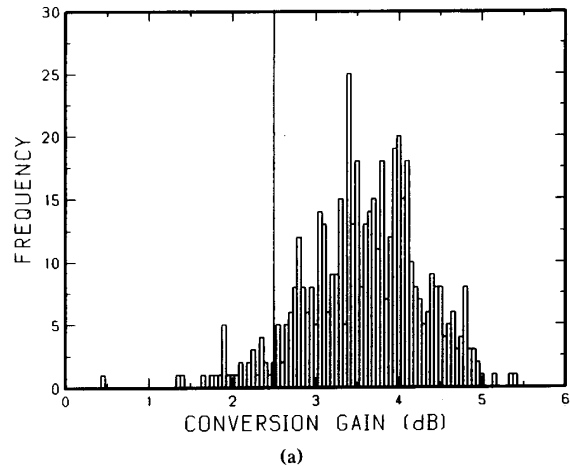


Fig. 3. Histogram of conversion gains of the frequency doubler based on 500 statistical outcomes. The specification is shown by a vertical line. (a) At the starting point. (b) At the solution of yield optimization using IGAT. (c) At the solution of yield optimization using FAST.

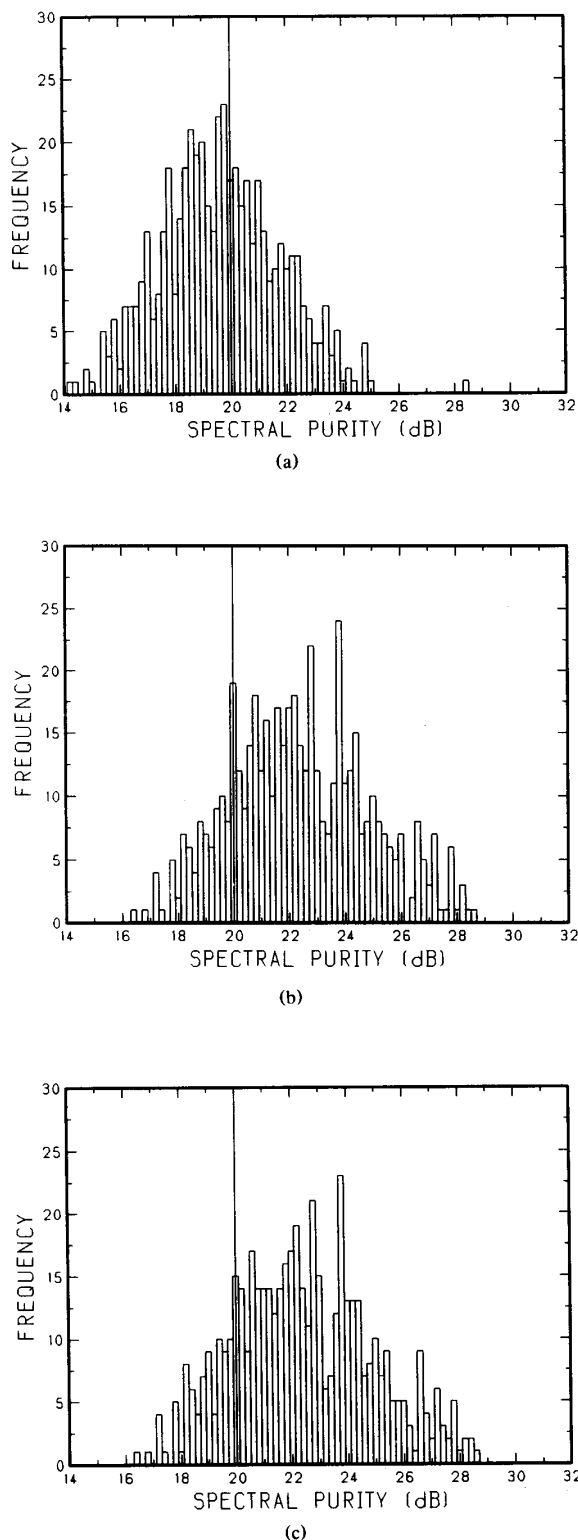


Fig. 4. Histogram of spectral purities of the frequency doubler based on 500 statistical outcomes. The specification is shown by a vertical line. (a) At the starting point. (b) At the solution of yield optimization using IGAT. (c) At the solution of yield optimization using FAST.

## VII. CONCLUSIONS

This paper presents a comprehensive formulation for yield optimization of nonlinear circuits operating within the harmonic balance simulation environment. We have conducted a convincing demonstration of yield optimization of statistically characterized nonlinear microwave circuits using our two best approaches to gradient calculation, namely, IGAT and FAST. These two approaches are expedient tools for gradient calculation in the HB environment. The significant advantages of IGAT and FAST over PAST are their unmatched speeds, and over EAST are their implementational simplicity. IGAT is a desirable choice when the circuit simulator cannot be modified. FAST is particularly suitable for implementation in general-purpose microwave CAD software.

Numerical experiments directed at yield-driven optimization of a FET frequency doubler verify our two gradient calculation approaches. Large-signal FET parameter statistics are fully facilitated. The substantial computational advantages of IGAT and FAST have been observed. Our approaches provide powerful tools to meet the very pressing need for efficient microwave nonlinear circuit design. Our success should strongly motivate the development of statistical modeling of microwave devices for large-signal applications.

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